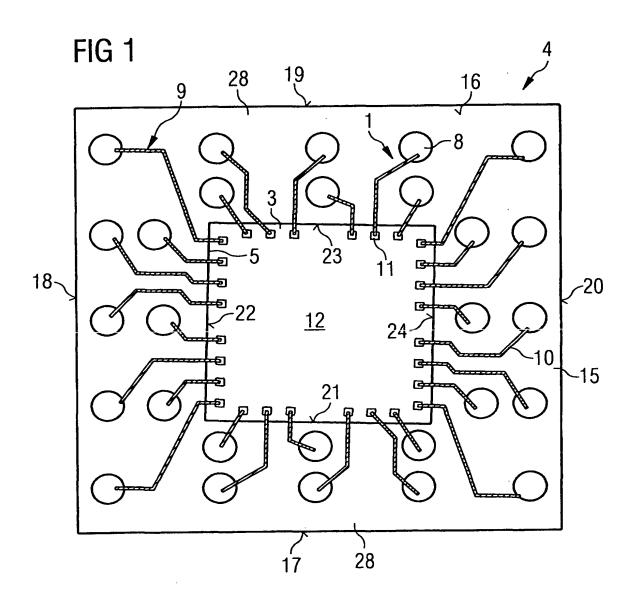
1/2



Docket No. 1431.146.101/FIN 506 PCT/US
METHOD FOR APPLYING REWIRING TO A PANEL WHILE
COMPENSATING FOR POSITION ERRORS OF SEMICONDUCTOR
CHIPS IN COMPONENT POSITIONS OF THE PANEL
Harry Hedler et al.

